

Study of CZTSSe based solar cells with different ETMs by SCAPS

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Abstract

Third-generation thin-film solar cells based on CZTSSe are highly promising because of their excellent optoelectrical properties, earth-abundant, and non-toxicity of its constituent elements. In this work, the performance of CZTSSe based solar cells with TiO₂, CdS, and ZnSe as electron transporting materials (ETMs) was numerically investigated using the Solar Cell capacitance Simulator (SCAPS). The effect of the active layer's thickness and electron affinity, different buffer layers and the contour plot of the operating temperature versus thickness of the CdS buffer layer were studied. The results show that the optimum power conversion efficiency for CdS, TiO₂ and ZnSe, as the ETMs, is 23.16%, 23.13%, and 22.42%, respectively.

Keywords: CZTSSe, Third-generation thin-film, SCAPS, efficiency, ETMs.

I. INTRODUCTION

Photovoltaic cells are used to convert large amounts of sunlight to electricity directly. Many studies have recently been conducted to improve the efficiency of thin-film solar cells (TFSCs). Because of their extraordinary properties, TFSCs based on CZTSSe and related materials have attracted increased interest as an absorber layer in third-generation photovoltaic devices. CZTSSe is a p-type conductivity semiconductor with a tunable direct bandgap of 0.95-1.5 eV, a large absorption coefficient of over 10⁴cm⁻¹, a low cost (earth-abundant), and non-toxic element composition [1]. The certified power conversion efficiency (PCE) of CZTSSe was reported to be up to 12.62%, which is lower than that of CIGS and CdTe (23.4% and 22.1%, respectively). It should be improved for large-scale photovoltaic applications to overcome the scarcity of In, Ge, and Te, as well as the toxicity of Cd [2-5].

There has been lots of research into replacing toxic ETM (CdS) with alternative materials in CZTSSe-based TFSCs. As a result, we propose and simulated three device structures with buffer layers of CdS, TiO₂, and ZnSe using SCAPS-1D software. The effect of

several parameters on device performance (V_{oc} , J_{sc} , FF, and PCE) was investigated, including thickness, electron affinity, various buffer layers, and temperature.

II. Methodology

The schematic cross-section of the TFSC structure used in this study is shown in Fig. 1. The device under investigation consists of a Mo back contact that serves as the positive terminal, the p-CZTSSe active layer in which electron-hole pairs are generated after absorption of incident photons. Following that, CdS, TiO₂, or ZnSe ETM was used to align the absorber and the window layer. The buffer layer is then stacked with i-ZnO, which is capped by a ZnO:Al window layer that acts as a transparent conductive oxide (TCO) to collect charges.

SCAPS is a program developed at the University of Gents in Belgium [6]. It is widely used for the simulation of various types of TFSCs. The SCAPS simulation results have been reported to agree well with the corresponding experimental results, which provides a convincing reason to use them in this study [4]. The software is based on solving the fundamental semiconductor equations ((1), (2), and (3)), namely the Poisson equation and the hole and electron continuity equations. It computes the band diagram in a steady-state, the recombination profile, and carrier transport in one dimension. The equations are shown below [4,7,8].

$$\frac{\partial^2 \Psi}{\partial x^2} + \frac{q}{\epsilon} [p(x) - n(x) + N_D - N_A + \rho_p - \rho_n] = 0 \quad (1)$$

$$\frac{1}{q} \frac{dj_p}{dx} = G_{op}(x) - R(x) \quad (2)$$

$$\frac{1}{q} \frac{dj_n}{dx} = -G_{op}(x) + R(x) \quad (3)$$

Here q is the electron's charge, ϵ is the dielectric constant, Ψ is the electrostatic potential and, N_A (N_D) is the density of acceptor-like (donor-like). p (n), ρ_p (ρ_n), and J_p (J_n) are hole (electron) concentration, hole (electron) density, electron distribution, and hole (electron) current density, respectively. R is the net recombination from direct and indirect recombination, and G_{op} is the optical generation rate.

The values of the device and material parameters used in this study are taken from the literature, experimental, theory, and reasonable estimation [7,8,10] and are summarized in Table 1.

The device was illuminated with an AM 1.5 spectrum with a light power of 1000 W/m². This study's shunt and series resistances were 600 Ω /cm² and 1.5 Ω /cm², respectively [4].

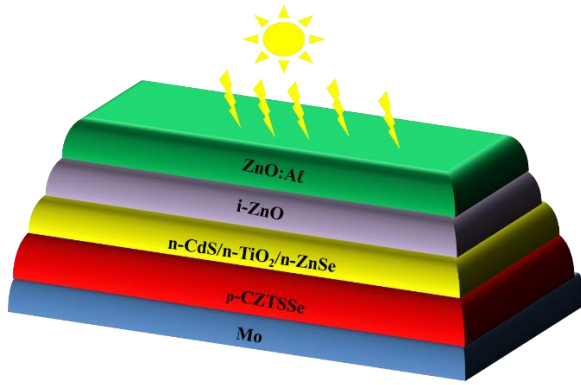


Fig.1. Structure of CZTSSe cell

Table.1 Parameters used in the simulation [7,8,10]

Parameters	ZnO:Al	i-Zno	n-TiO ₂	n-ZnSe	n-CdS	p-CZTSSe
d (nm)	200	50	50	50	Variable	Variable
E _g (eV)	3.3	3.3	3.26	2.90	2.4	Variable
χ (eV)	4.4	4.4	4.2	4.02	4.2	Variable
ε (eV)	9	9	10	10	10	13.6
N _c (cm ⁻³)	2.2x10 ¹⁸	2.2x10 ¹⁸	2.2x10 ¹⁸	2.2x10 ¹⁸	2.2x10 ¹⁸	2.2x10 ¹⁸
N _v (cm ⁻³)	1.8x10 ¹⁹	1.8x10 ¹⁹	1.8x10 ¹⁹	1.8x10 ¹⁹	1.8x10 ¹⁹	1.8x10 ¹⁹
V _{th e} (cm/s)	10 ⁷	10 ⁷	10 ⁷	10 ⁷	10 ⁷	10 ⁷
V _{th p} (cm/s)	10 ⁷	10 ⁷	10 ⁷	10 ⁷	10 ⁷	10 ⁷
μ _n (cm ² /Vs)	10 ²	10 ²	10 ²	25	10 ²	10 ²
μ _h (cm ² /Vs)	25	25	25	100	25	25
N _D (cm ⁻³)	10 ²⁰	10 ¹⁹	10 ¹⁸	10 ¹⁸	10 ¹⁷	0
N _A (cm ⁻³)	0	10 ¹⁹	0	0	0	10 ¹⁸
α (cm ⁻¹)	[4]	[4]	[10]	[10]	[4]	[4]

d: Thickness, E_g: Bandgap, χ: Electron Affinity, ε: Dielectric permittivity, N_c: Density of states in CB, N_v: Density of states in VB, V_{th e}: Thermal velocity of electron, V_{th p}: Thermal velocity of hole, μ_n: Electron mobility, μ_h: Hole mobility, N_D: Donor density, N_A: Acceptor density, α: Absorption coefficient

III. Results and discussion

A. Effect of the CZTSSe absorber layer's thickness

The absorber layer is crucial in enhancing device efficiency. In this context, simulations with CdS, TiO₂, and ZnSe buffer layers were used to examine the solar cell's performance in terms of the CZTSSe absorber layer. The thickness of the CZTSSe the absorber layer varied from 500 nm to 3000 nm, with a fixed ETM thickness of 50 nm. As the thickness of the CZTSSe absorber layer increases, more

photons are absorbed, resulting in more electron-hole pairs [10]. Fig.2. depicts the variation of photovoltaic parameters (V_{oc}, J_{sc}, FF, PCE) as a function of CZTSSe absorber layer thickness, in which the result is in good agreement with Beer-Lamberts law. Table 2 summarizes the changes in all device parameters caused by various ETMs for absorber thicknesses of 500 nm and 3000 nm.

Table. 2 The effect of the ETM layer on the photovoltaic parameters for absorber thicknesses of 500 nm and 3000 nm.

Buffer layer	CZTSSe thickness (nm)	Voc (V)	Jsc(mA/cm ²)	FF (%)	PCE (%)
CdS	500	0,6708	29,55026	74,78	14,82
	3000	0,7245	42,6436	74,96	23,16
TiO ₂	500	0,6706	29,37618	74,7	14,72
	3000	0,7245	42,6101	74,93	23,13
ZnSe	500	0,6703	29,45534	70,24	13,87
	3000	0,7243	42,58405	72,68	22,42

B. Effect of the electron affinity of the absorber layer

Figure 3 depicts the effect of absorber layer CZTSSe electron affinity on photovoltaic cell performance. The electron affinity of the absorber layer varied from 4,35 eV (CZTSe) to 4,5eV (CZTS) as extracted from the reference [4]. We can see that VOC, FF, and PCE values increase until a maximum value of χ=4,41eV is reached, then decrease with further increase of χ. On the other hand, Jsc decreases linearly with the electron affinity. Because increasing the absorber layer's electron affinity reduces the number of photons reaching the absorbing layer, the amount of current generated, and the short circuit current decreases.

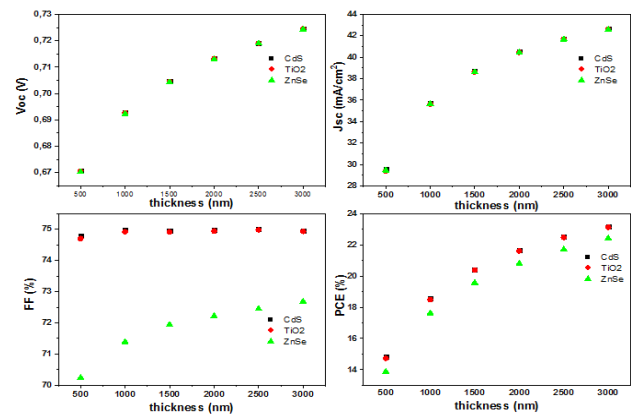


Fig.2. Effect of various thickness of CZTSSe absorber layer with the different buffer layers

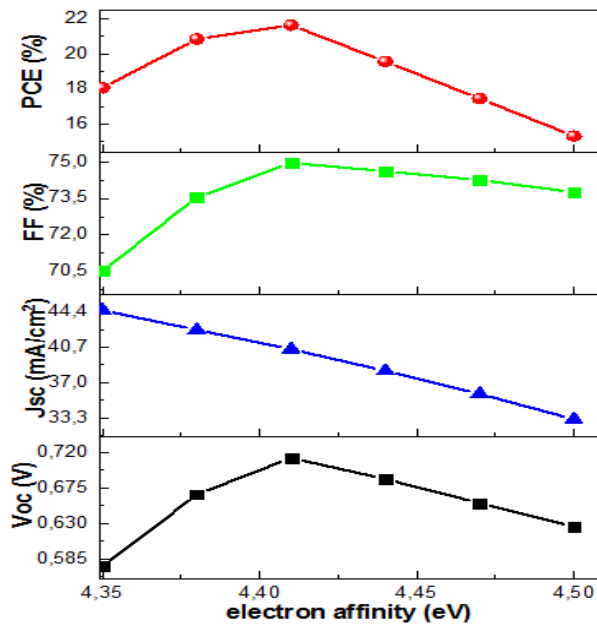


Fig.3. Variation of PCE, FF, J_{sc} , and V_{oc} as a function of CZTSSe electron affinity

C. Impact of the CdS buffer layer thickness and Temperature contour plot

The buffer layer and operating temperature are well known to affect the performance of solar cells. As a consequence, the CdS buffer layer thickness and operating temperature have been increased to improve performance, from 30 nm to 70 nm and from 240 °C to 320°C, respectively. As shown in Fig. 4, the all-output parameters are not affected by the thickness of CdS. On the other hand, the increase in temperature induces a decrease in PCE, FF, and V_{oc} ; this decrease was significant from the ambient temperature of 300°C. However, J_{sc} rises as the temperature rises.

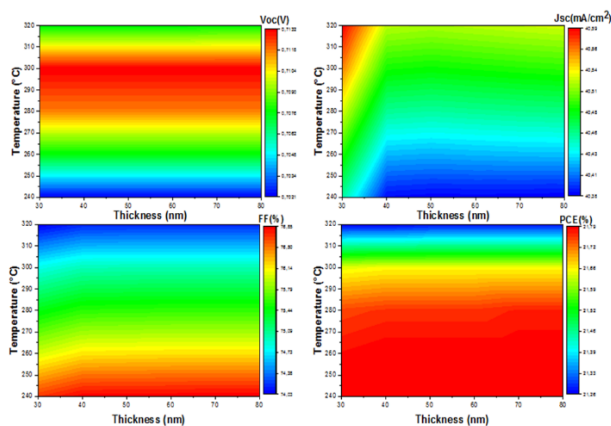


Fig.4: Contour plot of solar cell output as a function of the CdS buffer layer thickness and operating temperature.

IV. Conclusion

In this paper, numerical simulations of CZTSSe-based TFSCs were performed using the SCAPS-1D software. Three different solar cells with buffer layers of CdS, TiO₂, and ZnSe were investigated in order to find a safe alternative to the toxic CdS. Maximum PCE of CZTSSe solar cells with CdS, TiO₂, and ZnSe buffer layers was predicted to

be 23.16%, 23.13%, and 22.42% at ambient temperature and 3000 nm active layer, respectively. As a result, TiO₂ may be a viable choice for producing and manufacturing low-cost, high-efficiency Cd-free CZTSSe heterojunction solar cells.

V. ACKNOWLEDGMENTS

The authors are very much grateful to Dr Burgelman for providing SCAPS-1D software.

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